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T-763 P.002/003 F-903

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James J. Bade (Depositor's name)  
\_\_\_\_\_  
(Signature)  
May 25, 2006 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/809,044	03/25/2004	Shiro Fujima	NEKU 21.077 (100806-00256)	5789

TITLE OF INVENTION: REDUNDANCY CONTROL CIRCUIT WHICH SURELY PROGRAMS PROGRAM ELEMENTS AND SEMICONDUCTOR MEMORY USING THE SAME

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	06/07/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
MAI, SON LUU	2827	365-200000

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- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.  
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Elpida Memory, Inc.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Tokyo, Japan

01 FC:1501 1400.00 DA  
02 FC:1504 300.00 DA  
03 FC:8001 30.00 DA

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Typed or printed name

Brian E. Hennessey

Date

May 24, 2006

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51,271

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## Katten Muchin Rosenman LLP

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To	Company	Fax Number	Phone Number
1. Commissioner for Patents	United States Patent and Trademark Office	(571) 273-2885	

Date	Client/Matter Number
May 25, 2006	100806-00256
From	Attorney Number
James J. Dade	03290
Phone	Fax
212.940.6731	(212) 940-8986

Total number of pages, including cover letter: 3  
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Comments:

U.S. Patent Application No.: 10/809,044

REDUNDANCY CONTROL CIRCUIT WHICH ...

Our Reference No.: NEKU 21.077 (100806-00256)

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